

RNA53A27F

R03DS0022EJ0300 Rev.3.00 Jun 26, 2013

System Organizer IC for Dedicate Microcomputer

Description

RNA53A27F is reset function and reference voltage output for microcontroller has been installed.

Provide support for Renesas MCU operation.

Features

Reset detection voltage: 2.745 V ± 1%
Standard voltage: 3.050 V ± 0.8%

• Delay time: 50,100 ms (fine regulation by resistance is possible)

• Corresponds to manual reset

• Sleep mode is installed

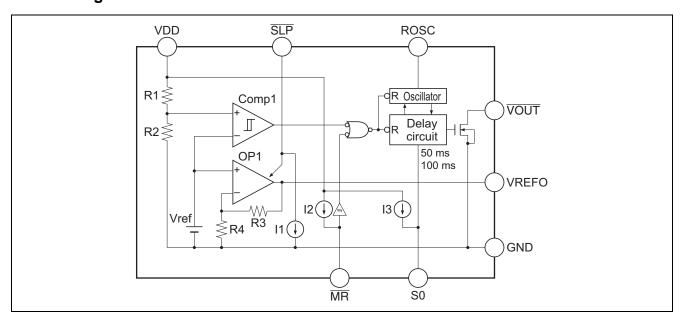
Ordering Information

			Package	Taping Abbreviation	Surface
Part Name	Package Type	Package Code	Abbreviation	(Quantity)	Treatment
RNA53A27FUSH1	SSOP-8	PVSP0008KA-A	US	H (3,000 pcs/reel)	1 (Sn/Bi)

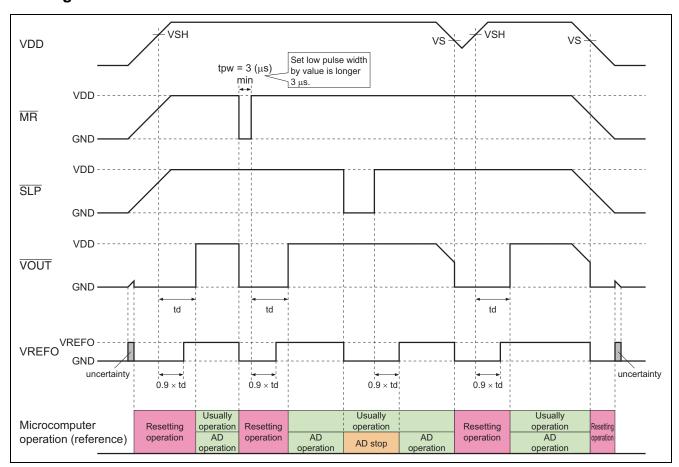
Application

- Power supply voltage monitoring for microprocessors
- Computers and notebook computers
- Digital still camera, digital video camera, and PDA

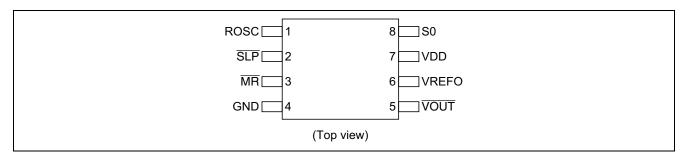
Block Diagram



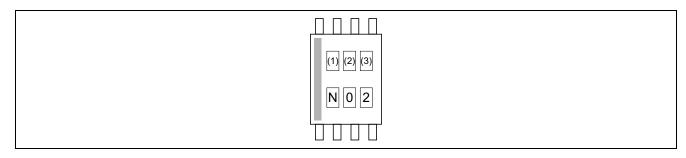
Timing Chart



Pin Arrangement



Mark Indication



(1)	Year code	The last digit of year
(2)	Month code	Starting in January "A", "B", "C", "D", "E", "F", "G", "H", "J", "K", "L", "M"
(3)	Week code	View week of month, 1 week \rightarrow "1"

Pin Description

	Terminal			
Pin No.	Marking	Terminal Name	1/0	Function
1	ROSC	Oscillation control resistance	_	• 1.1 M Ω is installed between GND usually.
		installation terminal		 Delay adjustment range of resistance: 650(kΩ) to 1.35(MΩ)
2	SLP	Sleep mode control	I	L: Sleep mode (Power-supply current becomes several μA)
				H: Usually mode
				Terminal is open, Pull-down in GND
3	MR	Manual reset input	- 1	Low is input, 5pin(VOUT) output the low level
				Set the low pulse width by value is longer 3 μs
				Terminal open, pull-up in VDD
4	GND	GND	_	Sorcing 0 V.
5	VOUT	Reset output	0	Power-supply voltage reaches the value below the detection
				voltage, low is output
				Low level is input to MR terminal, low is output
				Open drain output
				 Range pull-up resistance: 10(kΩ) to 100(kΩ)
6	VREFO	Standard voltage output	0	Standard voltage (3.05 V typ) for AD/DA is output
				Outside puts capacity (0.1 μF) by pursuant for microcomputer
7	VDD	Power-supply terminal	_	Sorcing power-supply voltage.
8	S0	Delay time switch	I	Delay time is switched. Low: 50 (ms), High: 100 (ms)
				Terminal is open, pull-up in Vdd

Absolute Maximum Ratings

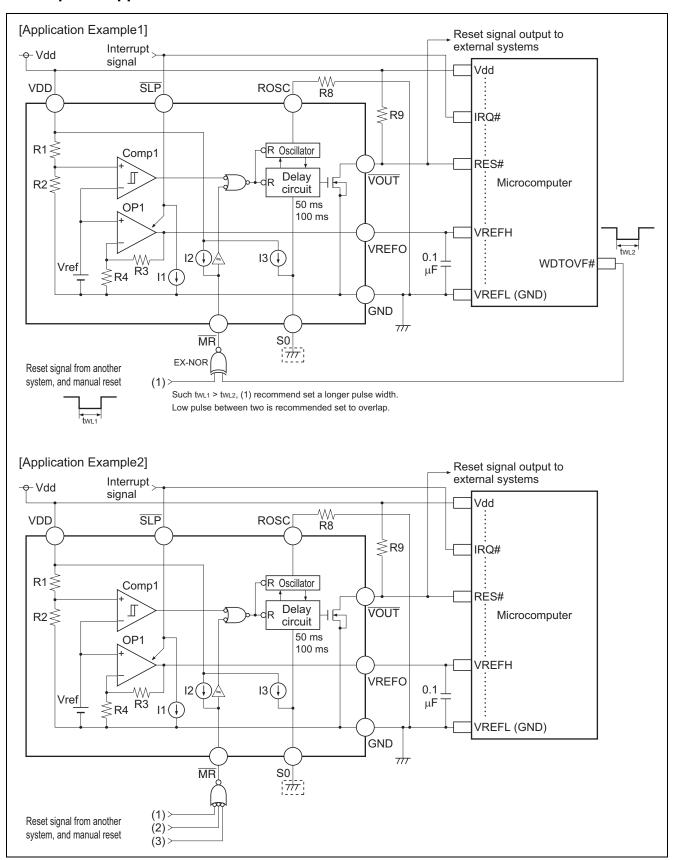
Item	Symbol	Ratings	Unit
Supply voltage	VDD	6.5	V
Output voltage (open-drain type)	Vout	-0.3 to +6.5	V
Input voltage	Vin	-0.3 to VDD	V
Output current	lout	10	mA
Standard voltage terminal source current	IREF	5	mA
Continuous power dissipation	Pd	160 (Ta = 25°C)	mW
Operating temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	−55 to +125	°C

Electrical Characteristics

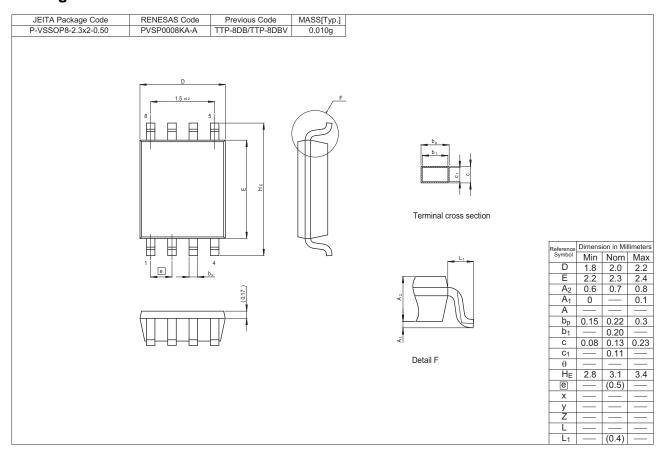
 $(Vdd = 3.3 V, Ta = 25^{\circ}C, unless otherwise noted)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Output fixation voltage	VOUL	_	_	0.9	V	VOUT terminal, RL = 10 (kΩ)
Operating limit voltage	VOPM	2.7	_	_	V	
Current consumption	ldd	_	100	200	μΑ	Vdd = 3.3 V, SLP = Hi (Vdd)
Standing circuit current	IddSLP	_	2.8	5.6	μΑ	$\overline{SLP} = Low (GND), \overline{MR} = Hi (Vdd),$ $\overline{SO} = Hi (Vdd)$
Detecting voltage	VS	2.718	2.745	2.772	V	
Detecting voltage temperature coefficient	Vs/∆T	_	±100		ppm/°C	
Hysteresis voltage	VHYS	VS×0.03	VS×0.05	VS×0.08	V	
Standard voltage	VREF	3.026	3.050	3.074	V	Vdd \geq 3.3 V, Isource = 0 to 3 mA, CL = 0.1 μ F
Standard voltage temperature coefficient	VREF/ ΔT	_	±100	_	ppm/°C	
Standard voltage return time	VRCV	_	_	0.9×td	ms	
Output low voltage	VOL	_	0.3	0.54	V	VOUT, Isink = 4 mA, Vdd = 2.6 (V)
Output leakage current	ILK	_	_	100	nA	VOUT, Vdd = 3.6 V
Minimum pulse width	tpw	3	_	_	μs	MR terminal input
Delay time	td	35	50	65	ms	S0 = Low
		70	100	130	ms	S0 = High
Reactive time	tPHL	_	6	_	μs	VDD drop to time until outputting VOUT MR input to time until outputting VOUT
High input voltage	VIH	Vdd×0.8	_	Vdd	V	MR terminal, SLP terminal, S0 terminal, Vdd = 3.0 V to 3.6 V
Low input voltage	VIL	0	_	Vdd×0.2	V	MR terminal, SLP terminal, S0 terminal, Vdd = 3.0 V to 3.6 V
S0 terminal pull-up current	IPUS0	_	_	0.5	μΑ	S0 terminal
MR terminal pull-up current	IPUMR	1.5	3	6	μА	MR terminal
SLP terminal pull- down current	IPDSLP	_	_	0.5	μА	SLP terminal

Example of Application Circuit



Package Dimensions



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